Appln. No. 09/883,438

Amdt. dated October 21, 2005

Reply to Office Action dated July 21, 2005

IN THE CLAIMS:

Please amend claims 1-10 and add new claims 11-20 as follows. The following listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

Claim 1 (Currently Amended). An integrated circuit comprising:

- a substrate,
- a conductive layer <u>arranged</u> in connection with the

5 <u>substrate</u>,

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layer and formed by a metallic turn having an outer outermost contour and an inner innermost contour, which bound between them a surface referred to as the a radiation surface, the turn of the at least one inductive element having a free space in a center defining an active zone which is not covered by the radiation surface, the active zone being receivable of capacitive, resistive or semiconductor elements, and

means for insulating the conductive layer from the <u>at least</u>

15 <u>one</u> inductive element, which integrated circuit is characterized

in that

the conductive layer has having a surface substantially

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identical to the radiation surface such that the conductive layer is situated around the active zone which is not covered by the radiation surface.

Claim 2 (Currently Amended). An integrated circuit as claimed in claim 1, characterized in that an wherein the active zone is integrated on a surface not covered by the radiation surface comprises capacitive, resistive or semiconductor elements.

Claim 3 (Currently Amended). An integrated circuit as claimed in claim 1, characterized in that wherein the conductive layer forms an open circuit includes bands separated by slots and which are oriented perpendicular to the at least one inductive element.

Claim 4 (Currently Amended). An integrated circuit as claimed in claim 1, characterized in that wherein the conductive layer includes conductive segments.

Claim 5 (Currently Amended). An integrated circuit as claimed in claim 4, characterized in that wherein the conductive segments are connected to a non-closed frame.

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Claim 6 (Currently Amended). An integrated circuit as claimed in claim 4, characterized in that wherein the substrate is formed with trenches perpendicular to the turn of the at least one inductive element, the bottoms of said trenches being covered with a low-resistance or conductive material, which forms the conductive layer.

Claim 7 (Currently Amended). An integrated circuit as claimed in claim 1, characterized by the presence of a well further comprising a frame of a low-resistance or conductive material [[,]] which well has walls surrounding the at least one inductive element completely, said well frame having at least one slot over its entire height.

Claim 8 (Currently Amended). An integrated circuit as claimed in claim 1, characterized in that it wherein the at least one inductive element comprises two inductive elements, which two elements are connected between a terminal at a given potential and a terminal that connects the inductive element element to a circuit, said inductive elements each being formed by a single and similar turn.

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Claim 9 (Currently Amended). An oscillator adapted to supply an output signal having a frequency whose value depends on the value of a tuning voltage, characterized in that it wherein the oscillator is realized in the form of an integrated circuit as claimed in claim 1, and it the active zone includes at least one varicap diode connected to the at least one inductive element and arranged to be biased by means of the tuning voltage.

Claim 10 (Currently Amended). An apparatus for receiving signals, comprising:

an antenna and filter system enabling the reception of a radio signal whose radio frequency , called radio frequency, is
selected within a given frequency range, and its conversion into
an electronic radio signal, called radio signal,

a local oscillator having a <u>frequency</u>, <u>called an</u> oscillation frequency , <u>which is</u> controllable by means of a tuning voltage,

a mixer adapted to receive the radio signal and a signal coming from the local oscillator and to supply an output signal having a fixed frequency equal to the difference between the radio frequency and the oscillation frequency, and

a signal processing unit adapted to utilize the output signal of the mixer, which apparatus is characterized in that

wherein the local oscillator is an oscillator as claimed in claim
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Claim 11 (New). An integrated circuit as claimed in claim

1, wherein the at least one inductive element is not superimposed on the active zone.

Claim 12 (New). An integrated circuit as claimed in claim 1, wherein the conductive layer includes bands separated by slots, the bands extending to but not into the active zone such that the active zone is surrounded by the bands.

Claim 13 (New). An integrated circuit as claimed in claim 1, wherein the turn of the at least one inductive element terminates at an innermost point over part of the conductive layer outside of the active zone.

Claim 14 (New). An integrated circuit as claimed in claim 1, wherein the conductive layer is formed to occupy only a portion of the total area of the integrated circuit and does not occupy a portion in a center of the integrated circuit including the active zone.

Claim 15 (New). An integrated circuit as claimed in claim 1, wherein the active zone is bounded by the innermost contour of the turn of the at least one inductive element.

Claim 16 (New). An integrated circuit as claimed in claim 1, wherein the active zone is formed on the substrate.

Claim 17 (New). An integrated circuit as claimed in claim 1, wherein the at least one inductive element is coiled around the active zone.

Claim 18 (New). An integrated circuit as claimed in claim 1, further comprising an insulating layer interposed between the at least one inductive element and the conductive layer, the insulating layer surrounding the active zone.

Claim 19 (New). An integrated circuit as claimed in claim 1, further comprising capacitive, resistive or semiconductor elements arranged on the substrate in the active zone.

Claim 20 (New). An integrated circuit comprising:

- a substrate,
- a conductive layer,
- at least one inductive element superposed on the conductive

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- layer and formed by a metallic turn having an outermost contour and an innermost contour, which bound between them a surface referred to as a radiation surface, the innermost contour defining an active zone on the substrate which is not covered by the radiation surface, and
- neans for insulating the conductive layer from the at least one inductive element, the conductive layer having a surface substantially identical to the radiation surface,

the at least one inductive element comprising two inductive elements connected between a terminal at a given potential and a terminal that connects the inductive elements to a circuit, said inductive elements each being formed by a single and similar turn.